

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Makoto YOSHIDA et al.

Serial No.: Not yet assigned

Filed: Even date herewith

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND  
MANUFACTURING METHOD THEREOF**

Group: 2815

Examiner: Edward Joseph Wojciechowicz

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR §1.97 & 1.98**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

September 4, 2003

Sir:

Pursuant to 37 CFR 1.97(b) and 1.98, attached hereto is one (1) form PTO-1449 listing the three (3) references which were cited by or submitted to the U.S. Patent and Trademark Office in prior applications Serial No. 09/446,302 filed April 14, 2000, and Serial No. 10/227,799 filed on August 27, 2002, the parent and grandparent applications of the present continuation application.

Pursuant to 37 CFR 1.98(d), copies of these three (3) references are not being provided with this Information Disclosure Statement because the references were previously cited by or submitted to the Office in the prior applications identified above and the prior applications are being relied upon in the present application for an earlier filing date under 35 USC 120 as required by 37 CFR 1.97(d)(1), and the references which were submitted to the Office in the prior applications were

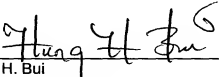
submitted in compliance with the version of 37 CFR 1.98(a)-(c) in effect as of the filing date of this Information Disclosure Statement as required by 37 CFR 1.98(d)(2).

Pursuant to 37 CFR 1.97(b)(1), this Information Disclosure Statement is being submitted within three months of the filing date of the above-identified national application.

It is respectfully requested that this Information Disclosure Statement be considered.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

  
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**FORM PTO-1449** U.S. Department of  
Commerce (Rev. 4/92) Patent and Trademark  
Office

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

ATTY. DOCKET NO.

**501.37959CC2**

SERIAL NO.

**Not yet assigned**

APPLICANT  
**Makoto YOSHIDA et al.**

FILING DATE  
Even date herewith

GROUP  
**2815**

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5	4	7	2	8	9	3						
									12/95	IIIDA et al.			

**FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
	0	9	0	9	7	8	8	2					YES	NO
	0	9	0	9	7	8	8	2	04/97	JAPAN				
	5	2	9	1	5	3	2	1993	JAPAN					

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**


EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])